

Electronic Patent Application Fee Transmittal

Application Number:	10570290			
Filing Date:	28-Feb-2006			
Title of Invention:	Intergrated circuit and a method of cache remapping			
First Named Inventor/Applicant Name:	Adrianus Josephus Bink			
Filer:	Mark Alan Wilson			
Attorney Docket Number:	NL031031			
Filed as Large Entity				
U.S. National Stage under 35 USC 371 Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Filing a brief in support of an appeal	1402	1	540	540
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				540